



NELENA

(News of Electrical Engineers Activities)

A Half Yearly News Letter

Department of Electrical And Electronics Engineering

PVP Siddhartha Institute of Technology
Autonomous & Permanent Affiliation to JNTUK, Kakinada
AICTE Approved, NBA Accredited and ISO 9001: 2008 certified Institution
(Sponsored by Siddhartha Academy of General and Technical Education)
Kanuru, Vijayawada- 520007

Volume: 3

July-December-2013

Faculty Incharge:

Mrs. D.Ragaleela

Mr. B.Mohan

Inside:

1. Faculty Publications

(i) Reviewed Papers

(i) International Journals

(ii) International Conferences

2. Faculty Achievements

3. Programmes attended

by staff (seminars / guest lectures / workshops)

4. Reaearch Projects Applied

5. Faculty Interaction

with outside World

6. Student Participation

7. Industrial Visit

Quotation:

*Inspiration and
genius-one and
the same.*

Victor Hugo

FACULTY PUBLICATIONS:

Reviewed Papers:

1. Mr. **B.MOHAN**, "Automatic Generation of power system single line diagram using graph drawing Algorithms", Journal Of Electric Power Components & Systems on Aug 30th 2013.
2. Mr. **B.MOHAN**, "Optimal Placement of Capacitors with Stand alone Voltage Control Systems in Radial Distribution Network", Electric Components And Systems Journal on 30th Dec. 2013.

International Journals:

1. Mr. **M.V.RAMESH**, "Wavelet Based Analysis Based Over current Protection for Permanent Magnet Brushless DC Motor", International Journal Of Soft Computing And Engineering, Volume-3, issue-4, September 2013. ISSN: 2231-2307.
www.ijscce.org/attachments/File/v3i4/D1832093413.
2. Mr. **M.V.RAMESH**, "Field Oriented Control for Space Vector Modulation Based Brushless DC Motor Drive", International Journal Of Advanced Research In Electrical, Electronics And Instrumentation Engineering, Volume – 2, Issue -9, September 2013 ISSN: 22320-3765.
http://www.ijareeie.com/upload/2013/september/12_-_Field.pdf.

3. Mrs. **K.BHAVANA** ,“Power Quality Control In Distributed Generation Systems using Shunt Active Power Filter”, International Journal Of Advanced Research In Electrical, Electronics And Instrumentation Engineering, Vol.2, Issue-9, September 2013, ISSN (Print) : 2320–3765, ISSN (Online): 2278–8875
http://ijareeie.com/upload/2013/september/30_POWER.pdf.
4. Mr. **B.BADDUNAIK**, “Pattern Recognition Approach for Fault Identification in Power Transmission Lines”, International Journal Of Engineering Research And Applications, ISSN: 2248-9622, Vol. 3, Issue 5, Sep-Oct 2013, pp.1051-1056
http://www.ijera.com/papers/Vol3_issue5/FZ3510511056.pdf.
5. Dr. **CH.PADMANABHA RAJU** “A Solution to Unit Commitment Problem via Dynamic Programming and Particle Swarm Optimization”, International Journal Of Current Engineering And Technology, ISSN: 2277-4106, Volume.3, Issue.4, October-2013, pp.1495-1503, Impact factor: 2.552(GIF), ICV:4.94.
<http://inpressco.com/wp-content/uploads/2013/10/Paper621495-1503.pdf>.
6. Dr. **S.V.M.BHUVANAIAKARAO**, “A voltage controlled adjustable speed induction motor drive using PFC half bridge converter” , International Journal Of Engineering Research And Applications (IJERA), ISSN: 2248-9622, vol.3, issue-6, Dec-2013.
7. Mr. **M.SESHU**, “Elimination of harmonic contents in a cascaded multilevel inverter using A adaptive hybrid technique”, International Journal Of Current Engineering & Technology (ISSN 2277-4106), volume 4, No.5, December 2013, pp.1606-1610, Impact factor: 2.552
<http://inpressco.com/category/ijcet/vol-3no-5dec-2013/>.

International Conferences:

1. Dr. **CH.PADMANABHA RAJU**, “A solution to unit commitment problem using particle swarm optimization”, IEEE Workshop On Computational Intelligence: Theories, Applications And Future Directions, July 2013 held at Indian Institute of Technology, Kanpur, India.
Http://www.iitk.ac.in/ee/archive/courses/2013/ci/workshopproceedings/SELECTED-20_PAPERS/abstractid130428-10.pdf.
2. Dr. **CH.PADMANABHARAJU**, “Power flow control and power flow studies with series FACTS devices”, IEEE Workshop on Computational Intelligence: Theories,

Applications And Future Directions, July 2013 held at Indian Institute of Technology, Kanpur, India.

<http://iitk.ac.in/ee/archive/courses/2013/ci/workshopproceedings/SELECTED-20PAPERS/abstractid130428-09.pdf>.

3. Mr. **M.V.RAMESH**, “Permanent Magnet Brushless DC Motor Protection against over currents using Wavelets”, IEEE CIS Workshop at IIT Kanpur July 2013.

<http://iitk.ac.in/ee/archive/courses/2013/ci/WorkshopProceedings/SELECTED%20PAPERS/abstractid130628-90.pdf>

4. Mrs. **J.HEMALATHA**, “Simulation & analysis of different parameters of various levels of cascaded H bridge multilevel inverter”, Microelectronics And Electronics (PRIME ASIA), 2013 IEEE Asia Pacific Conference on Postgraduate Research in IEEE from 19th -21st Dec. 2013, Page(s):62 - 67, Print ISBN: 978-1-4799-2750-0, INSPEC AccessionNumber:14080013,Visakhapatnam, DOI:10.1109/PrimeAsia.2013.6731179

<http://ieeexplore.ieee.org/xpl/mostRecentIssue.jsp?punumber=6720586>.

FAULTY ACHIEVEMENTS

The following faculty members registered for Ph.D at JNTUK, Kakinada in Nov 2013:

1. Mrs. **D. RAGALEELA**, Sr. Asst. Professor
2. Mr. **B. BADDU NAIK**, Asst. Professor
3. Mr. **M. RAVI KUMAR**, Asst. Professor

PROGRAMMES ATTENDED BY STAFF (SEMINARS / GUEST LECTURES / WORKSHOPS) :

1. Mrs. **M.DEVIKA RANI**, Ms. **G. MADHAVI**, Mrs. **A.VIJAYASRI**, Mr. **M. HEMANTH SAI**, Mr. **K.VIJAY KRISHNA**, and Mr. **B.BALASAIBABU** attended one week training programme on “Instruction Design and delivery Systems” from 8th -12th July 2013 with the collaboration of NITTR, Chennai organized by PVPSIT.
2. Dr. **S.V.M.BHUVANAIAKARAO**, Dr. **CH.PADMANABHARAJU**, Mr. **VIJAYA ANAND** and Mr. **M.V.RAMESH** attended one day workshop on “Outcome based engineering education” at PVPSIT by Dr.A.Koteswararao, Head, IT., VRSEC as resource person on 9th July 2013.
3. Mrs. **J.HEMALATHA** attended FDP (faculty development programme) on “Research Methodology” conducted by CSE dept. at GITAM School of technology during 3rd - 5th Oct 2013.

4. Mrs. **K. BHAVANA**, Mrs. **V.SAI GEETHA LAKSHMI** has participated in five day workshop on “Power Electronic Applications” organized by dept., of EEE, NIT Warangal from 14th – 18th Oct 2013.
5. Mrs. **A. VIJAYA SRI**, Mrs. **M. DEVIKA RANI** & Mr. **B. MOHAN** attended a two day National workshop on “Model based design for Power electronic drives and Embedded systems” conducted by RVR &JC college of Engg., Guntur on 12th – 13th Dec 2013.

RESEARCH PROJECTS APPLIED:

Projects Sanctioned: Dr. **M.V. RAMESH** (Co-ordinator), Assoc.Professor

Name of the Project	Sanctioning Authority	Duration	Amounts sanctioned Rs. In lakhs
Design and fabrication of permanent magnet brushless DC motor	SAGTE	2 years	0.9

FACULTY INTERACTION WITH OUTSIDE WORLD:

1. Dr. **S.V.M.BHUVANIKARAO** delivered a lecture on “Introduction to solar photo voltaic energy” at hotel Nagamani near N.T.R Health University to FOSWAL members (like citizens meet) on 28th July 2013.
2. Dr. **S.V.M.BHUVANAIKARAO** delivered a pep talk on “Energy conservation, environment and solar power” organized by The Institute of Engineers (India) on 17th December 2013 at KL Rao bhavan, Vijayawada.

STUDENT PARTICIPATION:

1. Ms. **CH.L.MADHUMATHI** and Ms. **K.MADHUMATHI** of IV B.Tech participated in “Personality Enhancement Programme” held at P.B. Siddhartha Arts & Science College Auditorium organized by Sri Satya Sai Seva Organization on 28th July 2013.
2. Mr. **N.S.SIVABALAJI** of IV EEE participated in “Cyber forensics and android security” conducted during 27th – 29th Dec 2013 at NIT Warangal.

INDUSTRIAL VISIT:

- I M.Tech students visited Dr. NTPS, Vijayawada on 23rd July 2013 accompanied with following faculty members: Dr. **SVM BHUVANA IKA RAO**, Mrs. **D.RAGALEELA**, Mrs. **A. VIJAYA SRI**, Mrs. **V. SAI GEETHA LAKSHMI** and Mr. **M. HEMANTH SAI**

Electrical and Electronics Engineering